

# MULTILAYER CHIP SUPPRESSORS (CHIP BEADS) 積層式晶片磁珠

## INTRODUCTION 產品介紹

Multilayer chip beads are SMD components that cover a wide impedance characteristic. Their impedance mainly comprises resistive part, so when a component is inserted in series with a noise conduction path, the noise content can be attenuated.

積層式晶片磁珠係表面黏著式晶片，涵蓋大範圍阻值，其阻抗主要由電阻性組成，所以當元件被安裝於一有雜訊傳導之通路，其雜訊可被衰減。

## FEATURES 特色

1. These small size chips generating high impedance.  
尺寸小同時產生高阻值。
2. Excellent solderability and high heat resistance for either flow or reflow soldering.  
優良的焊錫性及耐熱性，適合 flow 及 reflow 兩種鉚錫方式。
3. Monolithic structure for high reliability.  
單石結構確保高信賴性。
4. Low DC resistance structure of electrode prevents wasteful electric power consumption.  
電極之低直流阻值可避免不必要之電力消耗。

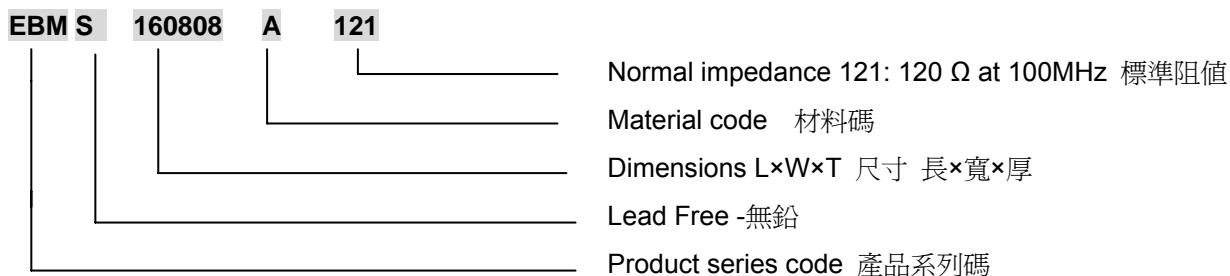
## APPLICATION 適用產品

To suppress EMI/RFI and to prevent self-oscillation in electronic products such as:

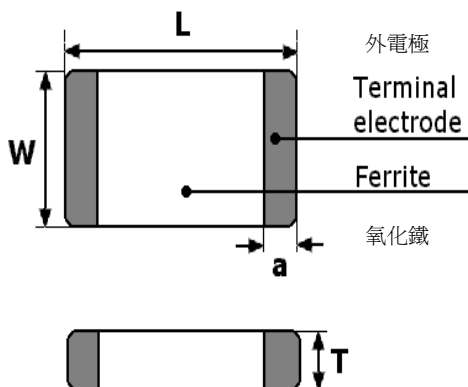
為抑制 EMI/RFI 及避免電子產品之自我振盪，例如：

1. Tablet, notebook, desktop computers and peripheral equipments. 平板、筆記型、桌上型電腦及其週邊設備。
2. Blu-ray DVD recorder, DSC, DVC, LCD Television. 藍光錄放影機、數位相機、數位攝影機、液晶電視機。
3. Mobile phone, smart phone. 行動電話、智慧手機。
4. Digital communication equipment. 數位通訊設備。
5. Various electronic equipments. 各式電子設備。
6. Circuit where a stable ground is unavailable. 缺乏穩定參考地之電路。

## PART NUMBERS SYSTEM 編碼系統



## SHAPES AND DIMENSIONS 形狀及尺寸



SHAPE 尺寸	L 長 mm	W 寬 mm	T 厚 mm	a 銀寬 mm
<b>060303(0201)</b>	0.6±0.03	0.3±0.03	0.3±0.03	0.1~0.2
<b>100505(0402)</b>	1.0±0.05	0.5±0.05	0.5±0.05	0.1~0.3
<b>160808(0603)</b>	1.6±0.15	0.8±0.15	0.8±0.15	0.2~0.6
<b>201209(0805)</b>	2.0±0.2	1.25±0.2	0.9±0.15/-0.2	0.2~0.8
<b>321611(1206)</b>	3.2±0.2	1.6±0.2	1.1±0.2	0.4~1.0
<b>321616(1206H)</b>	3.2±0.2	1.6±0.2	1.6±0.2	0.4~1.0
<b>322513(1210)</b>	3.2±0.2	2.5±0.2	1.3±0.2	0.6~1.0
<b>451616(1806)</b>	4.5±0.2	1.6±0.2	1.6±0.2	0.6~1.0
<b>453215(1812)</b>	4.5±0.2	3.2±0.2	1.5±0.2	0.6~1.0

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